

Bharati Vidyapeeth & Bharati Vidyapeeth Deemed University:

Bharati Vidyapeeth, the parent body of Bharati Vidyapeeth University was established by Hon'ble Dr. Patangrao Kadam on 10th May 1964 with the mission of 'Social transformation through dynamic education'. The University started functioning as a deemed University from 26th April 1996 and has earned reputation of being one of the top universities in India and achieved 'A' Grade university status by MHRD, Govt. of India (2012). The university also received 'A+' grade reaccreditation in (2017), 'A' grade accreditation in (2012) and accreditation in (2005) by NAAC.

BV (DU) College of Engineering, Pune:

College of Engineering, a constituent unit of Bharati Vidyapeeth (Deemed to be University), was established in 1983 and has 11 departments and runs courses at B.Tech, M.Tech and Ph.D levels. Further, few programs being conducted here were accredited by National Board of Accreditation (NBA), New Delhi. The remarkable efforts put in at improving the quality of education has lead the institute to selection by MHRD under Technical Education Quality Improvement Program-II (TEQIP-II). Recently ranked 131st out of 200 under NIRF rating in 2022. The college is ranked by MHRD GO. Ranked among Top 50 Private Institutions of India by ATAL Ranking of Institutions on innovation Achievements (ARIIA) successively for two years. Ranked 1st in DATAQUEST T-School Employability Index Survey 2022 and 3rd in West I Region DATAQUEST T-School Survey 2022

Founder Chancellor:
Hon. Dr. Patangrao Kadam
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Deemed University, Pune.

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Bharati Vidyapeeth
Deemed University, Pune
BVDU, Pune.

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ONE WEEK OFFLINE FACULTY DEVELOPMENT PROGRAMME ON

ADVANCEMENT IN VLSI DESIGN AND ITS APPLICATIONS

**Duration: 5th February to 10th February
2024**

Sponsored by

**AICTE Training and Learning (ATAL)
Academy**

Jointly Organized by
Department of Electronic and Communication
Engineering & Department of Electronic and
Telecommunication Engineering
Bharati Vidyapeeth (Deemed to be University)
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Bharati Vidyapeeth (Deemed to be University), College of Engineering, Pune



Objectives of FDP:

Semiconductor Design Technologies have gained immense popularity in the research community. The objective of this FDP is to provide research aspects and solutions to the various problems related to the semiconductor, VLSI Design & Modelling.

- Focus on emerging concepts of VLSI Design & Modelling.
- To provide a comprehensive overview of the fundamental concepts as well as latest CAD TOOLS (Cadence & Silvaco).
- To expand the in-depth knowledge of participants.
- To generate more open problems for research in related areas.
- To realize analog and digital circuit designs and its applications.

Who can attend?

The faculty members, post graduate students and Ph.D research scholars of the AICTE-approved institutions are eligible to attend this FDP Programme.

How to Register for FDP?

Eligible candidates have to register through the given link below:

<https://www.aicte-india.org/atal> or
<https://atalacademy.aicte-india.org/signup>

No Registration Fee

Confirmation of Participation and Certificates:

Upon the registration, confirmation will be sent to the selected participants through email on 20.01.2024.

The number of participants for this program is limited to 50 only.

The detailed schedule will be shared only to the confirmed participants through email and WhatsApp.

Certificate will be provided by the ATAL academy to those who have attendance more than 80%, filled daily feedback form and successfully completed the assessment test with score more than 60% at the end of the program.

Invited Resource Persons:

1. Dr. Udhyan Ganguly, Department of Electrical, IIT Bombay
2. Dr. Ashish Ranjan, Department of ECE, NIT Manipur
3. Dr. Bal Chand Nagar, Department of ECE, NIT Patna, Bihar
4. Dr. Ashok Kumar, Department of ECE, NIT Srinagar.
5. Dr. Shubhankar Majumdar, Department of ECE, NIT Meghalaya
6. Dr. Bhubon Chandra Mech, Department of ECE, DIAT Pune
7. Dr. Sandeep Mishra, Department of ECE, IIIT Pune
8. Dr. Arjun Singh Yadav, Department of ECE, NIT Rourkela, Orissa
9. Dr. Shankaranand Jha, Department of ECE, MREC, Hyderabad, Telangana
10. Dr. Prakash Pareek, Department of ECE, VIT, Bhimavaram, Andhra Pradesh
11. Dr. S. S. A. Askari, Department of ECE, IIIT, Ranchi, Jharkhand
12. Dr. Yogendra Kumar Upadhyaya, ITM Groups of Institutions, Aligarh, Uttar Pradesh