

Dr K K MEHTA

B E(Comp), M Tech(Comp), Ph D(Comp)

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Experience Details: Total 23 years

Associate Dean (Engineering Institute) NMIMS Shirpur

Since Feb 2021

Professor Computer Engineering since march 2020

Narsee Monjee University Shirpur Campus

Professor(CSE) & Founder Principal Since July 2018

Shri Shankaracharya Institute of Mass Communication

Professor (CSE)

Dean School of Engineering, Since Dec 2016

O P Jindal University Raigarh

Associate Professor CSE, 5th Sep 2013 to Dec 2016

NIRMA University Ahmedabad

Shankaracharya Technical Campus Bhilai

Professor HOD CSE, Sep 2000 to Dec 2012

Lecturer CSE, Aug 1998 to Sep 2000

Raipur Institute of Technology Raipur

Qualification:

Qualification	Board/University	Percentage
12 th Certificate Exam	M. P. Board Bhopal	67 % First Class in 1990
B. E. (Comp. Tech.)	KITS, RAMTEK Nagpur University	66 % First Class in 1994
M.Tech. (Comp. Tech.)	NIT Raipur	77 % First with Honours in 2002
Ph.D. (Comp. Sci. & Engg.)	C.S. V. Technical University Chhattisgarh Bhilai (C.G.)	in 2012
P G Diploma in Patents Law	"University of Law" Hyderabad, INDIA About to complete by 2021.	

International Association/Visit/Contribution:

1. Associated with World Congress in Applied Computing LAS VEGAS USA since 2006, to contribute towards organization of International Events.
2. Contributed as Co-Editor International Conference Proceedings for "Int Conference on Computer Design". 2006 LAS Vegas USA.
3. Steering Committee Member "International Conference World Congress in Applied Computing" June 2014, LAS VEGAS USA.
4. The International Conference on computer design and conference on computing in Nano technology CDES 2006. LAS VEGAS NEVADA USA June 26-29. CSREA Press. Responsibility as Associate Editor for International Conference Proceedings.
5. Presented Research Paper at SIAM University Bangkok Thailand on 20-23 Dec 2013, during IEEE International Conference on ICT and Knowledge Engineering.

Honours and Awards

1. Honoured by Chief Minister of Chhattisgarh State Dr. Raman Singh on 5th Sep 2010 for Excellence in Teaching, Academic Achievements, Promotion of Education, and Academic Administration.
2. Recipient of "Engineers Achievement Awards 2012", by Institution of Engineers Bhilai Local Center (C.G.), on 23rd December 2012.

Accreditation /Certification to the institute and programs:

- 1: National Board of accreditation (NBA) in the year 2005
Sri Shankaracharya College of Engineering and Technology Bhilai (C.G)
Awarded three years NBA
- 2: National Board of accreditation (NBA) in the year 2008
Sri Shankaracharya College of Engineering and Technology Bhilai (C.G)
- 3: National Board of accreditation (NBA) in the year 2011
Sri Shankaracharya College of Engineering and Technology Bhilai (C.G)
- 4: NAAC by UGC MHRD
NIRMA University Ahmedabad in the year 2015-16
Awarded NAAC "A" Grade

Research Publications in Brief:

Total 65 research papers published in journals including SCOPUS/Inder-science/UGC Care list/Springer. Total Citation index = 462, h-index = 11 and i-10 index = 11.

Funded Research Projects:

1. Completed Research Project funded by AICTE during 2009-2011 as Principal Investigator.
2. Minor Project funded by NIRMA University Ahmedabad during May 2015.
3. "Design and Development of Parallel Framework for Measurement of Heat Distribution for metallic Sheet" funded by Technical University under TEQIP-III.

Ph.D. Supervised/Awarded:

1. Seema Sahu "To Investigate and Develop Probability Function for MTBF in Object Oriented System" C. V. Raman University Bilaspur (C.G.).
2. Sanjivani Shantaiya "Development of Multiple Object Detection, Tracking, and Classification for Smart Video Surveillance" C. V. Raman University Bilaspur (C.G.).
3. Shubhangi Neware "Finger Knuckle based Biometrics for Personal Identification", registered at Dr. C. V. Raman University Bilaspur (C.G.).
4. Upasana Sinha "An application in Railway Derailment by Measuring Eye Blinking and Vibration Sensing for safe Driving" at Dr C V Raman University Bilaspur (C.G.).
5. Rajesh Tiwari "Optimization of SPMD Virtualization in Parallel Environment" C S V Technical University Bhilai Chattisgarh. Thesis Submitted.

Professional Activities:

1. Member "International Program Committee" World Congress in Applied Committee USA, during 2004 to 2007.
2. Member "Technical Committee" Nuicon International Conference NIRMA University, Nov 2015
3. Member "Reviewer Committee" IEEE International Cong Burla Odisha INDIA
4. Member "International Program Committee" IEEE International Conference at Galgotia University Greater Noida New Delhi.
5. Invited to receive Fellow Award during International Conference on VLSI by Dept of Electrical Engg IIT Mumbai on Jan 2014.
6. Member Executive Coinsil O P Jindal University Raigarh, since 2018 till date.

Membership of Professional Body:

1. Life Associate member “Institution of Engineers (India)”.
2. Life Member “Indian Society for Technical Education”.
3. Member “VLSI Society of India”.
4. Life member “Computer Society of India”.
5. Life Member “International Association of Engineers”.

Other Professional Contribution:

Editor in chief: Journal of Software Engineering Since 2012 i-manager Publications.

Business Incubation by MSME: I was looking after Entrepreneurship Cell setup by department of MSME Government of India. I was also looking after Skill Development Center under PMKY scheme at SSTC Bhilai.

Member Chief Advisory Board (Since 2013-2016)

1. International Journal of Engineering and Advanced Technology
2. International Journal of Soft Computing & Engineering
3. International Journal of Recent Technology & Engineering
4. International Journal of Innovative Technology & Exploring Engineering

Recent Activities:

1. Member “Academic Council” O P Jindal University.
2. Convener Industry Academia Conclave, 12th -13th April 2018
3. Chief Editor “*Journal on Software Engineering*”
4. Member “*Research Progress Committee*” Institute of Technology NIMRA University Ahmedabad.

Events Organized:

1. International Conference on Computer Design (CDES'07) Technical session “*Low Power Computer Design*” going to held in May 2007 in LAS VEGAS USA.
2. International Conference on Computer Design (CDES 06) 24-26 June 2006 held in MonteCarlo resort Las Vegas USA.
3. International Conference in Computer Design World congress in Applied Computing LAS VAGAS USA in 21-26 June 2006. as Session chair, Paper reviewer and Co-editor.
4. Organized a National Level Technical event “*National level technical symposium SAVVY 2005*” for faculty/professional and students on Jan 2005, and worked as Organizing Secretary.
5. National Level Seminar on “*Role of Science & Technology for Sustainable Development of Chhattisgarh State*” 25-26 Jan 2012, as Organizing Secretary.
6. International Conference on “*Technology Enabled Organizational Transformation TRANSLIENCE 2012*” Organized by Shri Shankaracharya Technical Campus Bhilai (C.G.), on 21-23 June 2012 as Organizing Secretary.
7. 3rd International conference on “*Advances in Steel Power and Construction Technology*” by O P Jindal University on 22-23 march 2017 as Core committee Member.

M.E. Dissertation Supervised:

1. Pradeep Mishra "*Low Power Modeling using Self and Coupling Transition*" Session 08-09, C S V Technical University BHILAI (C.G.).
2. Rajesh Tiwari "*Concurrent Solution of SPMD problem using MATLAB*" Session 08-09, C S V Technical University BHILAI (C.G.).
3. Visha Laxhi "*Artificial Immune Recognition System Support for Thyroid Diagnosis using Voice Analysis*" Session 08-09, C S V Technical University BHILAI (C.G.).
4. Sunil Singh "*Object Oriented Inheritance Matrix using RSM Tools*" Session 08-09, C S V Technical University BHILAI (C.G.).
5. Akkanchha S. Choubey "*Overhead Calculation of Unidistance Bus Codec in DEC Alpha 21264 A CPU*" Session 09-10, C S V Technical University BHILAI (C.G.).
6. Shreejit Paniker "*Steago Analysis on Cover and Steago Images*" Session 09-10, C S V Technical University BHILAI (C.G.).
7. Saurabh Agrawal "*Study and Analysis of Fruit Plant Recognition System using Image Warping and Neuro Fuzzy Technique*" Session 10-11, C S V Technical University BHILAI (C.G.).
8. Anand Tamrakar "*Analysis of Effectiveness of Web Based e-Learning through Information Technology*" Session 10-11, C S V Technical University BHILAI (C.G.).
9. Upasana Sinha "*Implementation of Derailment Avoidance based on NADALS Theory using Oscillation Monitoring System*" Session 11-12, C S V Technical University BHILAI (C.G.).
10. Mukesh Kumar "*A Modified Method to Segment Sharp and Unsharp Edge of Brain Tumor in 2D MRI*" Session 11-12, C S V Technical University BHILAI (C.G.).
11. Rashi Bais "*Biometric based Encryption key Generation*" Session 11-12, C S V Technical University BHILAI (C.G.).
12. Pushpalata Verma "*Exploring chances of Designing Vedic Multiplier for Power Conscious Design*" Session 11-12, C S V Technical University BHILAI (C.G.).
13. Riju bhattacharya "*Derailment Avoidance based on NADALS Theory using Oscillation Monitoring System for Multiple Profiles*" Session 11-12, C S V Technical University BHILAI (C.G.).

Industrial Training/AICTE/STTP attained:

1. Workshop on "*Intellectual Property Right*" on 17th march 2012 at Faculty of Engineering & Technology (SSGI) in collaboration with Ministry of Micro, Small, and Medium Enterprises Government of INDIA.
2. International workshop on "*VLSI Design*" Organized by VLSI society on INDIA, 2nd-7th Jan. 2010 in NIMHANS centre Bangalore.
3. Training Program on "*Computer Hardware Maintenance and Networking*" one month training organized by CEDTI (Centre for Electronic Design Technology India) Chandigarh April-May 1996.
4. Sort Term Training Program on "*Parallel Algorithm and Architecture for High Performance Computers*" organized by Govt. College of Technology Coimbatore, Coimbatore Tamilnadu, on 24th May-6th of June 04.
5. Sort term training program on "*DSP Tools and Practice*" Organized by G.S. Sanyal School of Telecommunications IIT Kharagpur, during 06-11 June 2005.
6. Workshop on "*OrCAD and Applications*" by Cadence EDA software, on Jan 2005 in Department of Electronics Engineering. SSCET BHILAI (C.G.).

7. Workshop on “*Lab View Programming*” organized by Shri Shankaracharya College of Engineering & Technology Bhilai delivered by Trident Tech labs Pune during 21-25 Sep 2012.
8. Workshop on “*Virtual Instrumentation through Lab View*” organized by IIPC Department of Electronics and Telecommunication, Shri Shankaracharya College of Engineering & Technology Bhilai 3 rd December 2008.
9. Workshop on “*Intelligent Computing Technique in Data Mining*” organized by Maulana Azad National Institute of Technology, Bhopal (M.P.) on 14th Jan to 18th Jan 2013.
10. Workshop on “*MATLAB APPLICATIONS*” organized by Department of Electrical & Electronics and Electronics and Telecommunication Engineering, Shri Shankaracharya College of Engineering & Technology Bhilai during 04-10 May 2006.
11. ISTE Approved STTP on “Big Data: Data Exploration and Visualization”, 30 June – 11 July 2014. NIRMA University Ahmedabad as Coordinator.

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