YENISI - V: WINTER - 2016

Subject : I.T. Elective-III a) Design of Language Processors

Day: Friday Time: 10.00 A.M. TO 1.00 P.M. S.D.E. Date: 09/12/2016 Max Marks: 80 Total Pages: 1 N.B. Attempt any FIVE questions from Section - I 1) Attempt any TWO questions from Section - II. 2) 3) Figures to the right indicate FULL marks. Answer to both the sections should be written in the **SAME** answer book. 4) SECTION - I **Q.1** Discuss Context Free Grammar in detail. (10)What is Bottom-up Parsing? (10)Q.2Explain following terms in brief: (10)Q.3 Absolute Loader a) Relocated Loader b) Discuss work distribution in the passes of 2 Pass Assembler. (10)**Q.4** How Symbol Table is organized for Structured Languages? (10)Q.5 Explain Run-time Storage Administration in detail. (10)**Q.6** 0.7 Write short notes on the following (ATTEMPT ANY TWO): (10)Interpretive Languages a) Semantic Analysis b) BSS Loader c) SECTION - II What is Linking? Discuss various schemes of Linking in detail. (15)**Q.8** Compare and differentiate between Stream and Q.9 What is an Editor? (15)Structure Editors. $\mathbf{O}.10$ Explain the concept of Code Optimization along with appropriate (15)examples.

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