Proposed Structure of M.Tech Electrical Engineering (Power Systems) CBCS Pattern (2015-16)

STRUCTURE & EXAMINATION PATTERN

Semester I								Total 1	uration: 2 Marks :50 Credits: 1		k
Subjects	Schen	Teaching Scheme (Hrs) Hrs./Week		Examination Scheme (Marks)					ieme	Total Credit s	
	L	P	Theory	Unit Test	Attend ance	Tutorial/as signments	TW	Pract/ Oral	TH	TW/PR /OR	
Research Methodology	04		60	20	10	10	-		04	-	04
FACTS and HVDC	04		60	20	10	10	-		04	-	04
Advanced Microcontroller & Its Applications	04	02	60	20	10	10	25	25	04	01	05
Power System Modeling	04	02	60	20	10	10	25	25	04	01	05
Total	16	04	240	80	40	40	50	50	16	02	18

Semester II								T	tal Duratio otal Marks otal Credit		eek
Subjects	Schem	ching e (Hrs) Week			Examinatio (Mar				Sc	nination heme redits)	Total Credits
	L	P	Theory	Unit Test	Attendan ce	Tutorial/ assignme nts	TW	Pract/ Oral	ТН	TW/PR/ OR	
Power Systems Dynamics	04		60	20	10	10			04	-	04
Digital Protection of Power System	04	02	60	20	10	10	25	25	04	01	05
PLC & SCADA	04	02	60	20	10	10	25	25	04	01	05
Elective - I	04		60	20	10	10			04		04
Total	16	04	240	80	40	40	50	50	16	02	18

Semester III								To	tal Durat	ion: 28 hrs	s/week
								T	otal Mar	ks : 500	
								T	otal Cred	lits: 40	
Subject	Teac	hing			Examination	on Scheme			Exam	ination	Total
	Schem Hrs./									neme edits)	Credits
	L	P	Theory	Unit Test	Attenda nce	Tutorial/ assignme nts	TW	Pract/ Oral	TH	TW/PR /OR	
Power Quality Issues	04	02	60	20	10	10	25	25	04	01	05
Elective –II	04	02	60	20	10	10	25	25	04	01	05
Self-Study Paper-I	04		60	20	10	10	-	-	04	-	04
Dissertation Stage –I	ı	07	-	ı			25	25		21	21
Seminar	1	05	-	-			25	25	-	05	05
Total	12	16	180	60	30	30	100	100	12	28	40

Elective – I	Elective - II
a) Power Sector Restructuring & Deregulationb) Power system planning & reliability	a) Advanced Control system b) Advanced Power Electronics & Drives

Semester IV								T		ion: 14 h rks : 325 dits: 34	rs/week
Subject	Schem	ching e (Hrs) Week		Examination Scheme				Examination Scheme (Credits)		Total Credits	
	L	P	Theory	Unit Test	Attendanc e	Tutorial /assignm ents	TW	Pract/ Oral	TH	TW/P R/OR	
Self-Study Paper-II	04		60	20	10	10	-	-	04	-	04
Dissertation Stage –II	-	10	-	-		-	150	75		30	30
Total	04	10	60	20	10	10	150	75	04	30	34

List of Self Study paper I & II

Self Study Paper I	Self Study Paper II
Condition Monitoring of Electrical Equipments	Electrical Power Capacitors
Energy Storage Devices	Nano technology & its applications in Electrical
	Engineering
Digital Measurement Techniques	High voltage insulation system & design
Energy Conservation & Audit	Use of synchronized measurement techniques in
	power system
Solar PV & Wind energy systems	Distributed Generation
Demand response & demand side management	Smart Grid - Automation System for State
	Transmission Utility
Digital Signal Processing Applications in Power	Substation design
Systems	

		RESEARCH METHODOLOGY		
TEACHI	NG SCHEME:	EXAMINATION SCHEME:	CREDITS ALLOTTEI)·
Theory: 0	4 Hours / Week	End Semester Examination: 60 Marks	04 Credits	,
		Continuous Assessment: 40 Marks		
UNIT - I	Fundamentals			(08 Hours)
		h Characteristics, Research Need, Objective on and objectives – Research methods vs	• •	
		 Descriptive vs. Analytical, Applied vs. Falitative, Conceptual vs. Empirical 	Fundamental,	
UNIT - II	Formulation of rese	arch problem		(08 Hours)
	Selecting the probl literature review in secondary sources – searching the web literature review – Technical Paper –su	ion — Defining and formulating the researcem - Necessity of defining the problem — defining a problem — Literature review — reviews, treatise, monographs-patents — wo - Critical literature review — Identifying gas Development of working hypothesis. Summary template, Online tools - Google, Ci	Importance of - Primary and eb as a source p areas from larizing a	
	Digital Library, I Searching patents	EEE, The on-line Computer Science	bibliography,	
UNIT - III	Research design me	thods		(08Hours)
	 Basic Principles- Important concept experimental designation of selecting design, different ty scales, sources or 	ampling design and scaling techniques – Research design — Features of gets relating to research design, basic has, implications of sample design, steps in segments amplied to sample design, steps in segments of sample design. Scaling techniques: ferror, technique of developing measure techniques, scale construction techniques.	good design – principles of ample design, ood sampling measurement	
UNIT - IV	Statistical analysis	T		(08 Hours)
	secondary data - Manalysis, statistics dispersion, measure	d analysis:- Observation and Collection of ethods of data collection, processing operation research, measures of central tendency res of asymmetry, measures of relation multiple correlation and regression, partial	tions, types of , measures of ships, simple	
UNIT - V	Research Paper & T	Thesis writing		(08 Hours)
	- Types of report - steps in the prepara - Illustrations and	s writing – Structure and components of sci - Technical reports and thesis – Significand tion – Layout, structure and Language of tables - Bibliography, referencing and foot ning – Preparation – Practice – Making prese	ce – Different typical reports tnotes - Oral	

	Use of visual aids - Importance of effective communication - Documentation and presentation tools: LATEX. Types of technical papers - Journal papers, Conference papers, Survey papers, Poster papers, Review papers Comparison, Structure of a survey, conference and journal paper, Organization and flow of thesis/ Project report, Research proposal: preparation, budgeting, presentation, funding agencies for engineering research,	
UNIT -	Research ethics, IPR and publishing	(08 Hours)
VI	Ethics: ethical issues.	
	IPR: intellectual property rights and patent law, techniques of writing a	
	Patent, filing procedure, technology transfer, copy right, royalty, trade related aspects of intellectual property rights Publishing: design of research paper, citation and acknowledgement, plagiarism tools, reproducibility and accountability.	
Text Book		
	thari, C.R., Research Methodology: Methods and Techniques. New Age Internat	
	rg, B.L., Karadia, R., Agarwal, F. and Agarwal, U.K., An introduction to Research	ch
	ethodology, RBSA Publishers	
	resh Sinha, Anil K Dhiman, Research Methodology, ESS Publications, Volumes 2	
	y R.A., How to Write and Publish a Scientific Paper, Cambridge University Pres	
	adehra, B.L. Law relating to patents, Trade Marks, copyright designs and geograp	hical
ind	lications. Universal Law Publishing	
D. C	D. I.	
Reference		7.1
	uis Cohen, Lawrence Manion and Keith Morrison, Research Methods in Education	on, /tn
	dition, Cambridge University Press, ISBN – 978-0415-58336-7	
	nthony, M., Graziano, A.M. and Raulin, M.L., Research Methods: A Process of quiry, Allyn and Bacon	
		dition
	anjit Kumar, Research Methodology: A Step by Step Guide for Beginners, 2nd EPH Publishing Corporation	aition,
	· ·	all
5. Fi	edy, P.D. and Ormrod, J.E., Practical Research: Planning and Design, Prentice Heink, A., Conducting Research Literature Reviews: From the Internet to Paper. Sag	a11
	illik, A., Conducting Research Enterature Reviews. From the internet to raper. Sag	50
	slie Lamport, 'Latex: A document preparation system' Addison Wesley, Reading	
	assachusetts, second	,
	or Unit Test:	
Unit Test -		
Unit Test -:		

		FACTS & HVDC			
TEACHING	G SCHEME:	EXAMINATION SCHEME:	CREDITS ALLOTT		
Theory: 04	Hours / Week	End Semester Examination: 60 Marks Continuous Assessment: 40 Marks	04 Credits		
UNIT - I	FACTS:			(08 Hours)	
	Conventional me reactors, Phase sl AC transmission converter structur topologies, Conventional issues	thods to increase transmission capacity, Senifting transformers, Synchronous condense controllers Basics, Challenges and needs, Seres, AC controller based structures, DC linearer output and harmonic control, Power	rs, Flexible tatic Power k converter		
UNIT - II		s Compensation:		(08 Hours)	
	Operation and co Capacitor, SVC, SVC, Power or resonance, TCSC TCSC, Static Syn				
UNIT - III	Unified Power F			[08 Hrs]	
	UPFC configuration, Independent real and reactive power flow control, Control scheme for UPFC, Basic control system for P and Q control, Dynamic performance, Operational constraints of UPFC, Power flow studies in UPFC embedded systems				
UNIT - IV	General Backgro	ound of HVDC Transmission:		(08 Hours)	
	HVDC link - Mo HVDC link, Equ phase six pulse a	s HVDC Transmission, Different configuration for Bipolar, Back to Back, Power flowation for HVDC power flow, Connection delay twelve pulse converter bridges, Voltage at of delay angle, Extinction angle, Overlap litage	ow through ns of three and current		
UNIT - V	Multi Terminal			(08 Hours)	
	Bipolar HVDC terminal, Converter transformer connections, Switching arrangements in DC yard for earth return to metallic return, HVDC switching system, Switching arrangements in a bipolar HVDC terminal, Sequence of switching operations, HVDC circuit breakers, DC current interruption, Commutation principle, Probable types and applications of HVDC circuit breakers, Multi-terminal HVDC systems, Parallel tapping, Reversal of power, Configurations and types of multi-terminal HVDC systems, Commercial multi terminal systems				
UNIT - VI	Protection and ((08 Hours)	
	Pole-wise segreg- reenergizing, Pro DC yards, Integr	mal condition in bipolar, Two terminal HVI ation, Protective zones, Clearing of DC line atection of converters, Transformer, Convertion of protection and controls, Hierarchical Lagram, Schematic diagram, Current controls	e faults and rter valves, al levels of		

control, DC voltage control, Commutation channel, Master control,	
Station control, Lead station, Trail station, Pole control, Equidistant	
firing control, Synchronous HVDC link, Asynchronous HVDC Link	

Text Books:

- 1. E.Acha, V.A.Agelidis, O.Anaya-lara and TJE MillerNewnes, Power Electronic control in Electrical Systems Oxford.
- 2. N.G. Hingorani and L.Gyugi, Understanding FACTS- IEEE Press, New York.
- 3. J. Arrilaga, Y.H.Liu and N.R.Watson, Flexible Power Transmission- The HVDC Options, John Wiley and sons Ltd., New York.

Reference Books:

- 1. T J E Miller, "Reactive Power Control in Electric Systems", John Wiley
- 2. Padiyar K R "FACTS Controllers in Power Transmission & Distribution", New Age.
- 3. R. Mohan and R.K.Varma, "Thyristor-Based FACTS Controllersfor Electrical Transmission Systems", IEEE Press.

Syllabus for Unit Test:	
Unit Test -1	UNIT – I, UNIT – II, UNIT – III
Unit Test -2	UNIT – IV, UNIT – V, UNIT – VI

	Advai	nce Micro controlle	ers and application	ons		
TEACHING	S SCHEME:	EXAMINATION S	SCHEME:	CREDITS ALLOTT		
Theory: 04 l	Hours / Week	End Semester Exam	ination: 60 Marks	04 Credits		
,		Continuous Assessn	nent: 40 Marks			
		TW&OR: 50 Marks	S	01 Credits		
		•		·		
UNIT - I	Introduction to I	PIC 16F8XX family a	nd development tool	s. CPU	(08 Hours)	
	architecture and	instruction set. Harvar	d architecture and p	oipelining,		
	program memor	y considerations, regis	ster file structure and	d addressing		
	modes, CPU reg	sters.				
UNIT - II	PIC peripherals				(08 Hours)	
	I/O ports, extern	al interrupts and time	ers, timer operation,	ADC, short		
	overview of sync	hronous serial port, ser	rial peripheral interfa	ce I2C bus.		
UNIT - III	Learning MPL	AB (V 5.0 or al	bove) Integrated of	development	(08 Hours)	
	environment fro	m Microchip (Asseml	bler and simulator),	Study of		
		motor control, temper				
	matrix keyboard	and LCD interfacing e	tc.			
UNIT - IV	ARM & AVR	Processors: RISC,	ARM design philos	ophy, ARM	(08 Hours)	
	fundamentals, i	nstruction set, thumb	instruction set, e	exception &		
	interrupt handlin	g, efficient C programi	ming, optimizing AR	M assembly		
	code, AVR arch	itecture, instruction s	et, hardware interfa	cing,		
	communication l	inks and design issues.				
UNIT - V	Interfacing c	onsiderations: Inte	l process con	nmunication,	(08 Hours)	
	synchronization	of processes, tasks, th	reads, devices &	buses for		
		are-software co-design				
	C/RT Linux	_	2 0			
UNIT - VI	Real time opera	ting systems: Survey	of software architec	tures- round	(08 Hours)	
		rupts, function queue			,	
		hitecture, task states,				
	_	sage queues, mailboxe		-		
		ment, interrupt routine				
		OS, embedded softwa				
	C/OS- II, VX wo		1	<i>^</i>		
				Į.		

Reference Books:

- 1. Microchip PIC family Microcontroller handbook
- 2. Design with PIC microcontrollers –John Peatman, Pearson Education Asia ,LPE
- 3. Rajkamal, 'Embedded system –architecture, programming and design', TMH Publication, edition 2003
- 4. David Simon," An embedded software Primer", Pearson education, Asia
- 5. Jonathan W. Valvano, Brooks, Cole" Embedded Microcomputer systems-Real time interfacing" Thomson Learning

Syllabus for Unit Test:	
Unit Test -1	UNIT – I, UNIT – II, UNIT – III
Unit Test -2	UNIT – IV, UNIT – V, UNIT – VI

		Power System Modeling		
TEACHING	SCHEME:	EXAMINATION SCHEME:	CREDITS ALL	OTTED:
Theory: 04Hours / Week		End Semester Examination: 60 Marks	04Credits	
		Continuous Assessment: 40 Marks		
		PR & OR: 50 Marks	01 Credits	
UNIT – I	Modeling of Non-Elect	rical Parameters:		(08 Hours)
	_	wer system analysis, Need for mathema	tical modeling of	
	-	ed models of non-electrical components s	-	
	steam & hydro turbine	, governor system		
UNIT – II	Modeling of Transform	ners:		(08 Hours)
	Transformer modeling	for two winding transformer, tap-change	er, phase shifting	
	transformer, three win	ding transformer and auto-transformer		
UNIT – III	Modeling of Transmiss	sion Line:		(08 Hours)
	Modeling of transmiss	ion network, Transformation to Alpha-B	eta components	
	using D-Q components	, Steady state equations		
UNIT – IV	Synchronous Machine	Modeling:		(08 Hours)
	Introduction, Park's T	ransformation, Flux Linkage Equation, Vo	oltage Equations,	
	Formulation of State-S	Space Equation, Current Formulation, Per	Unit Conversion,	
Normalizing Voltage equations, Normalizing Torque Equations, Torque & Power			Torque & Power	
	Equivalent Circuit of Synchronous Machine			
UNIT – V	NIT – V Excitation System Modeling:			
	1	tems, Control and protective systems, Mod	_	(08 Hours)
excitation systems (excitation system components and entire excitation system,				
	Voltage Response Ratio, Exciter voltage ratings			
UNIT – VI	Load Modeling:			
	-	oncepts, Static load representation, Dynam		(08 Hours)
	I	ion motor (as load) modeling, synchronous	motor (as load)	
		of load model parameters		
Text Books		. " 5 6 5 11:		
		ynamics", B.S. Publications		
		on Jr., "Power System Analysis ", 4 th Edition	i, McGraw Hill Inter	national
	nt Edition	System Theory - An Introduction", TMH Pul	aliahina Cammany 2	nd Edition
			onishing Company, 2	Edition
4. Kundu	i, rowei system bynam	nics & Control", IEEE Press, New York		
Reference	Books			
		system Control & Stability", Vol-I, IEEE Press	s New York	
		em Operation & Control"	o, INCAN FOIR	
۷. ۲.۵	. Waterly, Fower Syste	an operation & control		
Syllabus fo	or Unit Test:			
	or Unit Test:	UNIT – I, UNIT – II		
Syllabus for Unit Test -:	1	UNIT – I, UNIT – II UNIT – III, UNIT – IV		

		Power System Dynamic	cs		
TEACHING	SCHEME:	EXAMINATION SCHEME:		REDITS ALLO	OTTED:
Theory: 04 Hours / Week		End Semester Examination: 60		4 Credits	
		Continuous Assessment: 40 Ma	arks		
			1		
UNIT - I	Classical Methods of P	wer System Dynamic Studies			(08 Hours)
	Equality and inequality	onstraints in power system op	eration, state tran	sition	
	diagram, concept of sy	em security and stability, class	ical model of syste	em of one	
	machine connected to	finite bus, Clark diagram for tw	wo machines serie	S	
	reactance system, exte	sion of Clark diagram to cover	any reactance net	work,	
	elementary model of o	erall power system			
UNIT - II	Small Signal Stability:				(08 Hours)
		alysis of synchronizing & dam		-	
	_	implified synchronous machine			
		llation, improved model of s	ynchronous mach	ine, small	
	signal stability of multi	achine system			
UNIT - III	Large Signal Analysis:		 		(08 Hours)
	1	ansient stability, Large signa		_	
	•	methods (Modified Euler's,			
	· · · · · · · · · · · · · · · · · · ·	response, Analysis of unbalar	nced faults, Case	study of a	
	large system				(00.11)
UNIT - IV	Power System Stabilize				(08 Hours)
	Basic concepts of control signals in power system stabilizers (PSS), Structure and tuning, Field implementation, PSS design and application, Future trends				
UNIT - V		ation, F33 design and application	on, ruture trenus		(08 Hours)
OINII - V	Multi-machine system:		(US HOUIS)		
	Simplified model, Improved model of the system for linear load, Inclusion of load and SVC, Introduction to analysis of large power system				
UNIT - VI	Voltage stability:	analysis of large power system	11		(08 Hours)
Olvii - Vi		ting voltage stability & collans	a Analysis & com	narison of	(00 Hours)
	Definition, Factors affecting voltage stability & collapse, Analysis & comparison of angle & voltage stability and voltage instability & collapse, Control of voltage				
	instability, islanding - necessity, methods, advantages and disadvantages,				
		tem dynamic performance	, and alsaavantag	<i>cs,</i>	
	<u> </u>	,		l	
Text Book	s:				
		Control & Stability", IEEE press, New	York		
2. Oll	leElgerd, "Electrical Energy Sy	tem Theory - An Introduction", TMH			
Reference					
	R Padiyar, "Power System Dyn				
	abhaKundur, "Power system S	<u> </u>			
	W.Taylor, "Power System Vol	· · · · · · · · · · · · · · · · · · ·			
4. R.	A. Walling, "Distributed Gen	ntion Islanding", N.W. Miller			
-	or Unit Test:				
Unit Test -		UNIT – I, UNIT – II, UNIT – III			
Unit Test -2 UNIT – IV, UNIT – VI					

		Digital Protection of Power System		
TEACHING S	EACHING SCHEME: EXAMINATION SCHEME: CREDITS AI		CREDITS ALL	OTTED:
	Hours / Week	End Semester Examination: 60 Marks	04 Credits	
· ·	Continuous Assessment: 40 Marks			
		TW & OR : 50 Marks	01 Credit	
UNIT – I	Introduction:			(08 Hours)
	Need for Power syst	em protection, Digital Protection: State o	f Art, Merits of	
	•	ng scheme, Power System Components, Ba	•	
	Protection Scheme, Se	ction of Protection Scheme, Circuit Breakers a	and Relays, Types	
	and Applications. Archi	tecture of Modern Digital Relay		
UNIT - II	Static Relays:			(08 Hours)
	Introduction to Static F	Relay, Overcurrent Relay, Distance Relay, Prote	ection Schemes of	
	transmission lines, Swi	tched distance relay, Poly-phase relay, Relay	as Comparator -	
	Dual input Comparator	r, Relay characteristics by comparison of const	tants, Multi-input	
	comparator, Pilot Relay			
UNIT - III	Elements of Digital Pro			(08 Hours)
	-	digital relay, Signal conditioning subsystem: To	_	
	-	alog filtering and analog multiplexers, Conve		
		gital filter signal aliasing error, Sample and he		
		analog conversion, Analog to digital convers	sion, Digital relay	
	subsystem, Digital rela			(00.11)
UNIT – IV	Digital Protection of To			(08 Hours)
		ransmission line, Distance Relay, Travelling w		
	1 ·	sed on fundamental signal: hardware design,	~ .	
		HV/UHV transmission line based on travellin	g wave	
UNIT – V		ring scheme using amplitude comparison ransformer and Synchronous Generator:		(08 Hours)
OIIII V	_	Schemes used for Transformer Protection, Dig	rital Protection of	(00 110013)
	Transformer	ochemes used for Transformer Protection, Dig	gital Protection of	
		generator, Protection schemes for Synchronol	is generator	
	Digital Protection of Sy		as generator,	
UNIT – VI	•	n Power System Protection:		(08 Hours)
	_	t System (ES) for Protective Relay Settings: Inti	roduction,	
	· ·	S Approach, Typical Application, Fuzzy Logic (F	·	
	•	roduction, Problem Description, FL Approach, A	="	
		e Selection: Introduction, Problem Description		
	of fault generated in hi	gh frequency components, ANN Approach		
Text Books:				
_		ive Relaying from Electro-Mechanical to Micro	processor" By L.P. S	Singh. 2 nd
		Age International Publisher, New-Dehli.	red New Bellet	
		ction" By S.'. Bhide. PHI Learning Private Limi		A
Pub	lication: Institution of E	liques in Power Systems", By Kevin Warwick, A lectrical Engineers, London, UK.		
_	ital Protection for Power lectrical Engineers, Lond	r system" by A.T Johns and S.K. Salman. Peter I lon, United Kindom.	Peregrinus Ltd. Of T	he Institute
5. "Sof	t Computing Technique	s and its Applications in Electrical Engineering"	By Dr. Devendra Cl	naturvadi,

Publication: Springer – Verlag Be	rlin Heidelburg.		
Reference Books:			
 "Power System Protection 4: Digi Association. Published by Institu 	tal Protection and Signalling" edited by ETA Electricity Training te of Engineers, London, UK.		
"Digital Signal Processing in Power Szafran, Andrzej Wiszniewski.	2. "Digital Signal Processing in Power System Protection and Control" By Waldemar Rebizant, Janusz Szafran, Andrzej Wiszniewski.		
Syllabus for Unit Test:	yllabus for Unit Test:		
Unit Test -1	Init Test -1 UNIT – I, UNIT – II, UNIT - III		
Unit Test -2	UNIT – IV, UNIT – V, UNIT - VI		

		PLC and SCADA		
TEACHING SCHEME:		EXAMINATION SCHEME:	CREDITS ALL	OTTED:
Theory: 04 Hours / Week		End Semester Examination: 60 Marks	04 Credits	<u></u>
		Continuous Assessment: 40 Marks		
		Term Work: 50 Marks	01 Credits	
		•		
UNIT – I	Introduction to Pl			(08 Hours
	Definition & Histor	y of PLC, Overall PLC system, PLC Input and Outp	put modules, CPU,	
	· ·	supplies, PLC advantages and disadvantages, Se		
	· · · · · · · · · · · · · · · · · · ·	of Industrial Automation Systems, Process Cor		
		Introduction to Sequence Control, PLCs and Re	elay Ladder Logic,	
TINITE II	Hardware environr			(00 II
UNIT – II	PLC Programmin		D	(08 Hours
		pments, Construction of PLC ladder diagram,	•	
	•	adder diagram, Ladder logic, Functional block		
		e shooting, features, programming ON/OFF In letworking of Sensors, Actuators and Controllers:		
	Fieldbus Communic		The Fieldbus, The	
UNIT – III	PLC Applications			(08 Hours
01121 222		tion, PID control of continuous processes, si	mple closed loop	(00 110 111
		pop system using Proportional, Integral & Der	•	
		Controls: AC Motor starter, AC motor overload pr	• • • • • • • • • • • • • • • • • • • •	
	motor controller, Variable speed (Variable Frequency) AC motor Drive			
UNIT – IV	SCADA		(08 Hours	
	Need of SCADA system, Features, SCADA architecture – First generation, Second			
	generation, Third generation, HMI, MTU, RTU, IED's, 7 Layers of OSI, Communication			
	requirements for SCADA (communication protocols – DNP, IEC, Ethernet, TCP/IP,			
	Modbus, UDP), Client – Server based communication concept, SCADA Benefits			
**************************************				(00 TT
UNIT – V	SCADA in Power	•		(08 Hours
	Operation and control of interconnected power system, Automatic substation control, SCADA configuration, Energy Management System (EMS), system security,			
		ifiguration, Energy Management System (EMS), sy CADA system security issues overview	ystem security,	
UNIT – VI	Supervisory Mana	· · · · · · · · · · · · · · · · · · ·		(08 Hours
01111 - 11		environment with implementation examples, Sul	hstation	(00 Hours
		quipment condition monitoring using SCADA, Dist		
		ouble call management, Customer level intelligen	•	
	system, computer level monitoring and control of equipments			
	,	2		
Text Books:				
1. Terso	on, "Power System Co	ontrol Technology", Prentice Hall		
		"Control and Automation of Electric Power Dis	stribution Systems",	Taylor and
	cis, 2007			
Fran		nagement Handbook", 5 th Edition, 2004		
Fran 3. Turne	er, W. C, " Energy Ma	inagement Handbook", 5 th Edition, 2004 ion to Programmable Logic Controllers", Thomsor	n, 2 nd Edition	
3. Turno 4. Gary	er, W. C, " Energy Ma Dunning, "Introducti			5 th Edition

Reference Books:			
1. Handschin, E. "Energy Managem	1. Handschin, E. "Energy Management Systems", Springer Verlag, 1990		
2. Gordan Clark, Deem 'eynders, "Practical Modem SCADA Protocols"			
Syllabus for Unit Test:			
Unit Test -1	UNIT – I, UNIT – II, UNIT - III		
Unit Test -2	UNIT – IV, UNIT – V, UNIT - VI		

	(Electiv	re – I) Power Sector Restructuring & Deregular	tion	
TEACHING SCHEME:		EXAMINATION SCHEME:	CREDITS ALLOTTE	ED:
Theory: 0	4 Hours / Week	End Semester Examination: 60 Marks	04 Credits	
		Continuous Assessment: 40 Marks		
UNIT - I	Power Sector in India			(08
	Introduction to various	institutions in an Indian Power sector such as	CEA, Planning	Hours)
		FC, Ministry of Power, State and Central gover		
	•	ties and their roles. Critical issues / challenges		
	'	ry act 2003-Provision in the Generation, Transr	nission& Distribution	
	Sector, Various nationa	al policies and guidelines under this act.		
	Freedom antala af Franc	anciae O Dannau Cartau Danniatian		/00
UNIT - II		omics& Power Sector Regulation nomics applicable to Power Sector, Consum	aorhahaviar Cupplior	(08 (Hours
		librium, Short- run & Long- run costs, Various	• • •	Hours
	The state of the s	ge fixed cost (AFC), Average variable cost (AV	•	
		1C),Relationship between short-run and lon		
	-	market, Concept of life cycle cost , Annual rate		
	-	ernal Rate of Return(IRR) and Net Present V		
	Role of regulation and	evolution of regulatory commission in India, T	ypes and methods of	
	economic regulation, R	legulatory process in India.		
UNIT - III	Power Tariff			(08
		es (marginal cost, cost to serve, average cost),		Hours)
		ures and considerations, different consumer	•	
		ole charges, time of day, interruptible tariff,		
	-	centives etc., Subsidy and cross subsidy, life li tures for different load patterns. Government	·	
		it of renewable energy and captive po	•	
		tariff, Latest reformsand amendments	wer generation on	
	turni), iranabiney basea	tariii) Latest reformisana amenaments		
UNIT - IV	Power sector restructu	ring and market reform		(08
		sector restructuring, Reasons for restructuri	ing / deregulation of	Hours)
	power industry, Under	standing the restructuring process-Entities in	volved, The levels of	
	competition, The mar	ket place mechanisms and Sector-wise majo	or changes required ,	
	-	ctures and ownership models, Market models		
	,	oly Model, Single buyer Model, Wholesale co	•	
		odel, Marketarchitecture, Timeline for vari	• ,	
	-	stracts, The spot market, Models for trading	-	
		nd objectives of deregulation of various pow	•	
		, The Nordic Pool and The developing count	ries.Congestion	
UNIT - V	Management, Ancillary			(00
UNII - V	•	cing and Non-price issues Market Clearing price (MCP), Zonal and locat	ional MCDs Dynamic	(08 (Hours
		me pricing, Dispatch based pricing, Power flow	· ·	ilouis)
		es for real and reactive power. Unconstrain	· ·	
	· ·	pot prices.Non price issues in electricity res		
		JOL DITCES,NOTEDITCE ISSUES III ETECTTICITY TEX	LIUCLUIIIE MAANIV M	

	electricity reforms in different countries.	
UNIT - VI	Transmission Planning and Pricing	(08
	Transmission planning& operation in open access power systems, Introduction & Principles of transmission pricing, Differenttransmission pricingmethods, Transmission cost allocation methods, Marginal & Composite pricing Paradigms & their comparison, Introduction to transmission loss allocation & various methods of loss allocation, Debated issues in transmission pricing, Congestion issues and management, Ancillary Service Management, Forward ancillary service auction. Power purchase agreements.	Hours)

ReferenceBooks:

- 1. Loi Lei Lai, 'Power System 'estructuring & Deregulation, John Wiley & Sons Ltd.
- 2. "Know Your Power", A citizens Primer On the Electricity Sector, Prayas Energy Group, Pune
- 3. Sally Hunt, "Making Competition Work in Electricity", 2002, John Wiley Inc
- 4. Electric Utility Planning and Regulation, Edward Kahn, American Council for Energy Efficient Economy
- 5. D. S. Kirschen & G. Strbac, 'Fundamentals of Power System Economics', John Wiley & Sons Ltd.
- 6. Steven Stoft, Power System Economic Designing markets for Electricity, Wiley-Inter Science.
- 7. M Shahidepour, Hatim Yamin, Zuyi Li,'Market Operations in Electrical Power Systems, Forecasting, Scheduling and 'isk Management', Wiley Inter Science.

References:

- 1. Regulation in infrastructure Services: Progress and the way forward TERI, 2001
- 2. Maharashtra Electricity Regulatory Commission Regulations and Orders www.mercindia.com
- 3. Various publications, reports and presentations by Prayas, Energy Group, Pune www.prayaspune.org
- 4. Central Electricity Regulatory Commission, Regulations and Orders www.cercind.org
- 5. Electricity Act 2003 and National Policies www.powermin.nic.in
- 6. Market Operations in Electric Power Systems Forecasting, Scheduling and Risk Management Mohammad Shadepur, HatimYatim, Zuyi Li.
- 7. BhanuBhushan, "ABC of ABT A primer on Availability Tariff" www.cercind.org

Website: NPTEL-Phase II-

Syllabus for Unit Test:	
Unit Test -1	UNIT – I, UNIT – II, UNIT – III
Unit Test -2	UNIT – IV, UNIT – V, UNIT – VI

TE 4 6:	-	tive – I)POWER SYSTEM PLANNING AND RELIA		
			CREDITS ALLOTTED	<u>):</u>
	ory: 04 Hours / Week End Semester Examination: 60 Marks 03 Credits			
Practical: ()2 Hours / Week	Continuous Assessment: 40 Marks		
		Term Work: 25 Marks	01 Credit	
UNIT - I	Unit 1: Load Foreca	eting .		10
ONII - I	Onit 1. Load Foreca	sung:		(0 Hours
	· ·	ors affecting Load Forecasting, Load Res		
		ssification of Load and Its Characteristics, Load	-	
		(ii) Co-Relation Techniques, Energy Fore		
		ive Load Forecasting, Non-Weather sensiti	~·	
		load Forecasting, Annual Forecasting, Mont	,	
		ives & Factors affecting to System Planning,	Short Term Planning,	
		ning, Long Term Planning. [10 hrs]		10
UNIT - II	Unit 2: Probability	theory		(0 Hours
	Introduction to pro	bability, Probability distributions: Random	variables, density and	
	distribution function	ons. Mathematical expectation. Binominal	distribution, Poisson	
		l distribution, exponential distribution, Weibu		
Gaussian, Gamma and Beta distribution. Correlation and regression		on		
UNIT - III	Unit 3: Reliability			(0 Hour
	Reliability, Failure, (Concepts of Probability, Evaluation Techniques	(i) Markov Process (ii)	
	Recursive Technique, Stochastic Prediction of Frequency and Duration of Long & Short			
	· ·	acy of Reliability, Reliability Cost.	0	
UNIT - IV		Planning and Reliability:		
		s affecting Generation Planning, Generation So	ources, Integrated	
	Resource Planning, Generation System Model, Loss of Load (Calculation and			
	Approaches), Outage Rate, Capacity Expansion, Scheduled Outage, Loss of Energy,			
	Evaluation Methods	s. Interconnected System, Factors affecting inte	erconnection under	
	Emergency Assistan	ce.		
UNIT - V	Unit 5: Transmissio	n Planning and Reliability		(0
				Hour
		ng and Reliability: Introduction, Objectives of		
	Planning, Network Reconfiguration, System and Load Point Indices, Data required for			
	Composite System I	-		/-
UNIT - VI	Unit 6: Distribution	Planning and Reliability		(0 Hour
	Radial Networks -	Introduction, Network Reconfiguration, Ev	valuation Techniques,	
	Interruption Indices	s, Effects of Lateral Distribution Protection, E	ffects of Disconnects,	
	Effects of Protecti	on Failure, Effects of Transferring Loads, [Distribution Reliability	
	Indices. Parallel &	Meshed Networks -Introduction, Basic Evalua	ation Techniques, Bus	
	Bar Failure, Schedu	led Maintenance, Temporary and Transient Fa	ilure, Weather Effects,	
	Breaker Failure.			

Text Books:			
1. Roy Billinton& Ronald	N. Allan, Reliability Evaluation of Power System - Springer Publication.		
2. R.L. SullivanPower Syst	em Planning -, Tata McGraw Hill Publishing Company Ltd.		
3. Miler& Freund's, Proba	bility and Statistic for Engineers, Pearson Education, Richard Johnson.		
Reference Books:			
1. X. Wang & J.R. McDona	ıld, Modern Power System Planning –, McGraw Hill Book Company		
2. T. Gönen, Electrical Po	2. T. Gönen, Electrical Power Distribution Engineering - McGraw Hill Book Company		
3. B.R. Gupta Generation	3. B.R. Gupta Generation of Electrical Energy –, S. Chand Publications		
4. A.S. Pabla, Electrical P	4. A.S. Pabla, Electrical Power Distribution Tata McGraw Hill Publishing Company Ltd.		
5. T.W.Berrie, Electricity Economics & Planning –, Peter Peregrinus Ltd., London			
Syllabus for Unit Test:			
Unit Test -1	UNIT – I, UNIT – II, UNIT - III		
Unit Test -2	UNIT – IV, UNIT – V, UNIT - VI		

Power Quality Issues				
TEACHING	SCHEME:	EXAMINATION SCHEME:	CREDITS ALLOTTED:	
Theory: 04 Hours / Week		End Semester Examination: 60 Marks	04 Credits	
,		Continuous Assessment: 40 Marks		
UNIT - I	Voltage sag; swells and			[8Hrs]
	Introduction; importance of power quality; terms and definitions of power quality as per			
	IEEE std. 1159. Sources & Effects of Power Quality Problems; Sources of sag; swell and			
	interruptions; Estimation of voltage sag performance; Fundamental principles of			
	protection; solutions at end user level; utility systems and fault clearing issues; motor starting sags; evaluation of the economics of different alternatives.			
	Starting Sags, evaluation	if of the economics of different alternatives.		
UNIT - II	Transient Over- Voltag	es		[8Hrs]
	_	er voltages; capacitor switching; lightening; Fe	rro resonances and	
	other switching transie	nts; Principles of over voltage protections; de	vices of over	
		ility capacitor switching transients; Utility syst		
	protection; managing Ferro resonance; switching transients problems with loads;			
	computer tools for trar	isient analysis.		
UNIT - III	Fundamentals of Harn	nonics and its Analysis		[8Hrs]
OINII - III	Fundamentals of Harmonics and its Analysis Introduction; the Mechanism of Harmonic Generation; Definitions and Standards:			[опгэ]
	Factors Influencing the Development of Standards, Existing Harmonic Standards,			
	General Harmonic Indices.			
	Introduction to Harmonic Analysis; Fourier Series and Coefficients; Simplifications			
	Resulting from Waveform Symmetry; Complex Form of the Fourier Series; Convolution			
	of Harmonic Phasors; The Fourier Transform; Sampled Time Functions; Discrete Fourier			
	Transform (DFT); The Nyquist Frequency and Aliasing; Fast Fourier Transform (FFT); Window Functions; Efficiency of FFT Algorithms; Alternative Transforms.			
	Williadw Fullctions, Em	ciency of FFT Algorithms, Alternative Transfort	115.	
UNIT - IV	Harmonic Sources and	Distortions		[8Hrs]
		troduction; Transformer Magnetization Nonl	inearities Rotating	[01113]
		Distortion Caused by Arcing Devices; Single-		
		Source Conversion; Three-Phase Voltage-S		
	Thyristors-Controlled R	eactors.		
		Introduction; Resonances; Effects of Harm	_	
	· ·	rmonics on Static Power Plant; Harmonic Inter		
	System Protection; Eff Communications.	fect of Harmonics on Consumer Equipment;	interference with	
	Communications.			
UNIT - V	Computation, Assessm	ent and Harmonic Elimination		[8Hrs]
	•	: Introduction; Direct Harmonic Analysis; De	ivation of Network	[00]
	Harmonic Impedances	from Field Tests; Transmission Line Models	Underground and	
		d Models; Computer Implementation; Exampl	es of Application of	
	the Models;			
		Introduction; Filter Design Criteria; Network In	•	
	Performance Calculation	ons; Tuned Filters; Damped Filters; Convention	ai Filter	

	Configurations; Band-Pass Filtering for Twelve-Pulse Converters; Distribution System Filter Planning; Filter Component Properties; D.C. Side Filters; Active Filter	
UNIT - VI	Power quality monitoring; Assessment & Mitigation Need and approaches followed in power quality monitoring; objectives and requirements; Initial site survey; Power quality Instrumentation; Selection of power quality monitors; monitoring location and period; Selection of transducers; Harmonic monitoring; Transient monitoring; event recording and flicker monitoring. Power Quality assessment; Power quality indices and standards for assessment; waveform distortion; voltage and current unbalances; Power assessment under waveform distortion conditions. Power quality state estimation; State variable model; observability analysis; capabilities of harmonic state estimation; Test systems; Mitigation techniques at different environments.	[8Hrs]

References:

- 1. Understanding power quality problems; voltage sag and interruptions M. H. J. Bollen IEEE press; 2000; series on power engineering.
- 2. "POWER SYSTEM HARMONICS", Second Edition By Jos Arrillaga and Neville R. Watson; John Wiley and Publication, 2003 ISBN: 0-470-85129-5.
- 3. Electrical power system quality Pogei C. Dugan; Mark F. McGranghan; Surya santoso; H. Wayne Beaty; second edition; McGraw Hill Pub.
- 4. Power system quality assessment J. Arrillaga; M.R. Watson; S. Chan; John Wiley and sons.
- 5. Electric power quality G. J. Heydt.
- 6. Power system harmonics: Computer modeling and analysis- Enriques Acha; Manuel Madrigal; John wiley and sons ltd.
- 7. Power System Harmonics J. Arrillaga & N. Watson
- 8. IEEE std 519-1992/ IEEE std 1159 IEEE recommended practices and requirements for harmonics control in electrical power system.
- 9. ECBC Code 2007 (Edition 2008) published by Bureau of Energy Efficiency; New Delhi Bureau of Energy Efficiency Publications Rating System; TERI PUBLICATIONS GRIHA Rating System; LEEDS Publications

Syllabus for Unit Test:	
Unit Test -1	UNIT – I, UNIT – II, UNIT – III
Unit Test -2	UNIT – IV, UNIT – V, UNIT – VI

	(Elective – II) Advanced Control System			
TEACHING	SCHEME:	EXAMINATION SCHEME:	CREDITS AL	LOTTED:
Theory: 04 Hours / Week		End Semester Examination: 60 Marks	03 Credits	
Continuous Assessment: 40 Marks				
UNIT - I	· I PID Control:			(08 Hours)
	Review of c	lassical and modern control concepts: P	ID control and	
	tuning approaches, Selection of Variables for Control, PID Controller			
	Tuning for Dynamic Performance - Determining Tuning Constants for			
	Good Control Performance, Ziegler-Nichols method, Correlations for			
	Tuning Constants, Fine-Tuning the Controller Tuning Constants,			
	feedback	ning based on stability – Dead beat and s	elf tuning, Rate	
TINITE II				(00 TT
UNIT - II	State Variab	•		(08 Hours)
	•	m Analysis Using State Variable Methods		
	transfer function to phase variable and canonical variable model, Eigen			
	value and eigen vector, Kalman's test and Gilbert's Test for			
	controllability and observability analysis and design of control system in state space, Pole placement, State observer, Design of control system			
	with Luenberger observer			
UNIT - III	Nonlinear and Robust Control:		(08 Hours)	
	Nonlinear Sy	stems and Equilibrium Points, Concep	ts of Stability,	
	-	unction analysis, Phase plane analysis	•	
	Feedback Linearization, Input-output linearization, Input-State			
	Linearization			
	Concept of robust control, Description and categorization of system		•	
		System and signal norms, Small gain the		
TINITED TY	·	gn of robust control, Introduction to H-∞ c	OHUTOI.	(00 II)
UNIT - IV	Digital Cont		AC Ess s	(08 Hours)
		the Digital Control System, ADC, D.		
		continuous time signals, Quantization, San of signal, Sampling Theorem, Aliasi	_	
		signals, Impulse response, Linear conv		
		transform: Basics, Properties, Inverse Z		
		and partial fraction difference equation, S	_	
		th Jury's stability criteria	, ,	
UNIT - V	Frequency A	nalysis:		(08 Hours)

Frequency response of first order and second order systems, Polar plot,			
Bode plot, Bode plot from Sweep Frequency Response Analysis			
(SFRA) of transformer and its conclusion, Phase and group delays, Ideal			
filters and their pole zero locations, Zero phase and linear phase			
transfer functions			

Exponential representation of Fourier series and Fourier transform of continuous time signals, The Fourier series for discrete-Time periodic signals (only concept), The Fourier transform of discrete-time a periodic signals (only concept), Discrete Fourier Transform, Properties: Periodicity, Linearity, Symmetry properties, Circular convolution, Linear convolution using circular convolution, Fast Fourier Transform: Radix 2 DIT and DIF algorithms

UNIT - VI Optimal Control:

(08 hours)

Parameter optimization and optimal control problems, Hamiltonian formulation of optimal control problem, Hamilton-Jacoby equation, Linear regulator problem, Quadratic performance criterion, Numerical solution of Matrix Riccati equation, Pontryagin's minimum principle, Application to optimal control of discrete and continuous systems (quadratic performance index, analysis and design of finite and infinite time), Linear Quadratic Regulators, Introduction to Linear Quadratic Gaussian approach

Text Books:

- 1. 'Modern Control Engineering'- Katsuhiko Ogata, Prentice Hall India, 5th edition 2010.
- 2. 'Non-linear Systems', by Hassan Khalil, Prentice Hall.
- 3. Digital Control Ogata, Prentice Hall India

Reference Books:

- 1. Digtal Control- B.C.Kuo
- 2. 'Digital Control and State Variable Methods' by M. Gopal, Tata-McGraw-Hill Publishing Company Limited
- 3. Optimal Control: Linear Quadratic Methods' Brian D. O. Anderson, John Barratt Moore, Dover Publications, 2007

Syllabus for Unit Test:	
Unit Test -1	UNIT – I, UNIT – II, UNIT – III
Unit Test -2	UNIT – IV, UNIT – V, UNIT – VI

(Elective – II) ADVANCED POWER ELECTRONICS AND DRIVES				
TEACHING SCHEME:		EXAMINATION SCHEME:	CREDITS ALLOTTED:	
Theory: 04 Hours / Week		End Semester Examination: 60 Marks	04 Credits	-
Practical: 02 Hours / Week		Continuous Assessment: 40 Marks		
·		Term Work: 25 Marks	01 Credit	
UNIT - I Converters:				(08 Hours)
	Voltage Source Conver			
		vave bridge converter, operation and harmo	•	
	_	ters. PWM converter. Generalized technique		
	elimination and voltage control. Advanced modulation techniques (space vector			
	modulation, 3 rd harmonic PWM) Comparison of PWM techniques. Converter rating Current source converters			
			mathematical	
	(i) Matrix Converter: 3×3 matrix converter, principle of working, mathematical treatment, comparison of matrix converter with multipulse converter			
	(ii) Self and Line commutated current source converter: Basic concepts of CSC,			
	converters with self co	mmutating devices		
UNIT - II	Multilevel Inverters:			(08 Hours)
	Multilevel concept, Types of multilevel Inverters, diode clamped multilevel			
	inverter, flying-capacitors multilevel inverters, cascaded multilevel inverter,			
	switching device currents, D.C. link capacitor voltage balancing, features of			
	multilevel inverters, comparison of multilevel inverters. Applications of multilevel Inverter: Reactive power compensation Back to back intertie system			
UNIT - III	DC Drives:			(08 Hours)
	Single phase and 3 phase converter drives. Four quadrant Chopper drives, closed			,
	loop control of DC motor, Permanent magnet DC motor drives, DC Servo drives,			
	applications			
UNIT - IV	Induction Motor Drives:			(08 Hours)
	3 phase induction motor control, stator voltage control/rotor voltage control,			
	voltage and frequency control, current control, closed loop control of 3-phase			
	induction motor. Soft starters, comparison of variable frequency drives, Speed			
	control by static slip power recovery, induction motor servo drives, applications.			
UNIT - V	Synchronous Motor Drives:			(08 Hours)
	Voltage and frequency control, closed loop control of synchronous motors.			
	Synchronous motor servo drive with sinusoidal waveform, synchronous motor			
	servodrive with trapezoidal waveform. Load commutated invertor drives, speed			
	control of synchronous motors by cyclo-convertors, applications			
UNIT - VI	Akagi's p-q theory			(08 Hours)
	·	of active and reactive power in single phase an		
		of power with sinusoidal voltage source and n		
	•	ion of three phase four wire system-Akagi's ins		
	power (pq) theory-rela	tionship between Akagi's components and conv	ventional	
	active and reactive pov	ver application of pq theory to reactive and har	monic power	
	compensation in simple	e circuits.		

Text Books:

- 1. Bimal K Bose, Modern power electronics and AC drives, Pearson education asia
- 2. G. K. Dubey, Fundamentals of Electrical Drives CRC press 2002
- 3. VedamSubrahmanyam Electric Drives: Concepts & Appl Tata McGraw-Hill
- 4. Power electronics convertors, applications and design, Ned Mohan, Tore M Undeland, William P Robbins, Wiley India Pvt. Ltd., 2009
- 5 E. Acha, Miller & Others, Power Electronic Control in Electrical Systems (Newnes, Oxford publication) first Edition
- 6 M. H. Rashid Power Electronics, Prentice Hall of India Pvt. Ltd. New Delhi, (3rd Edition)
- 7. R Krishnan, Electric motor drives, modeling, analysis and control, PHI learning Pvt. ltd. 2001
- 8. S.K. Pillai, A first course in electrical drives, Newage international publishers. 2010

Reference Books and Papers:

- 1. E. H. Watanube, R.M. Stephen and Maurico Ardes "New Concepts of instantaneous active and reactive powers in Electrical systems with Generic loads" (IEEE transaction on Power Delivery Vol.8, no.2 April 1993, PP-697-703
- 2. L. Benchaita, S. Sadaate and A. Salemnia "A comparison of voltage source and current source shunt Active filter by simulation and Experimentation" (IEEE Transaction on Power Systems, Vol 14, No.2, May 99, PP 642-647
- 3. H. Akagi, E.H. Watanabe and M. Aredes "Instantaneous Power Theory and Applications to Power Conditioning, IEEE Press, New York

Syllabus for Unit Test:	
Unit Test -1	UNIT – I, UNIT – II, UNIT - III
Unit Test -2	UNIT – IV, UNIT – V, UNIT - VI